ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

A Muxed-Output Double-Date-Rate-2 (DDR2) Register with Fast Propagation Delay

Application Number:

Confirmation Number:

First Named Applicant:

Ke Wu

Attorney Docket Number:

PS-113

Art Unit:

Examiner:

Search string:

(6691272 or 6584578 or 6542416 or 6510100 or 6462596 or 6249483 or 5994935

or 5751174 or 5410194 or 5391935).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
AL	1	6691272	2004-02-10	Azim	B1	714	744
m	2	6584578	2003-06-24	Faue	B1	713	503
Ru	3	6542416	2003-04-01	Hampel, et al.	B1	365	194
xu	4	6510100	2003-01-21	Grundon, et al.	B1	365	233
M	5	6462596	2002-10-08	Varma	B1	327	218
M	6	6249483	2001-06-19	Kim	B1	365	233
KU	7	5994935	1999-11-30	Ueda , et al.	B1	327	202
W	8	5751174	1998-05-12	Kuo , et al.	B1	327	199
14	9	5410194	1995-04-25	Freidin , et al.	B1	325	46
AY	10	5391935	1995-02-21	Gersbach, et al.	B1	327	198

Signature

Examiner Name	Date
LUU	10/5/04